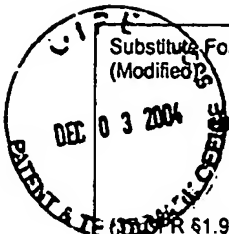
	Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-617001	Application No. 10/057,723
	<b>Information Disclosure Statement by Applicant</b> (Use several sheets if necessary)		Applicant Gilbert Wolrich et al.	
			Filing Date January 25, 2002	Group Art Unit 2122

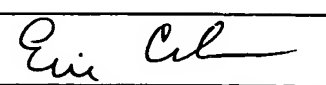
U.S. Patent Documents							
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
EC	AA	09/473,571	12/28/1999	Wolrich et al.	—	—	
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Examiner Initial	Desig. ID	Document Number	Publication Date	Country or Patent Office	Class	Subclass	Translation	
							Yes	No
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	AW							

Examiner Signature <i>Qui Al</i>	Date Considered 3/19/05
EXAMINER: Initials citation considered. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.	

 Substitution Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-617001	Application No. 10/057,723
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Other Documents (include Author, Title, Date, and Place of Publication)		
Examiner Initial	Desig. ID	Document
EC	AX	Byrd et al., "Multithread Processor Architectures," <i>IEEE Spectrum</i> , Vol. 32, No. 8, New York, 1 August 1995, pp. 38-46.
EC	AY	Doyle et al., <i>Microsoft Press Computer Dictionary</i> , 2 <sup>nd</sup> ed., Microsoft Press, Redmond, Washington, USA, 1994, p. 326.
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EC	ABB	Haug et al., "Reconfigurable hardware as shared resource for parallel threads," <i>IEEE Symposium on FPGAs for Custom Computing Machines</i> , 1998.
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Examiner Signature 	Date Considered 3/19/05
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